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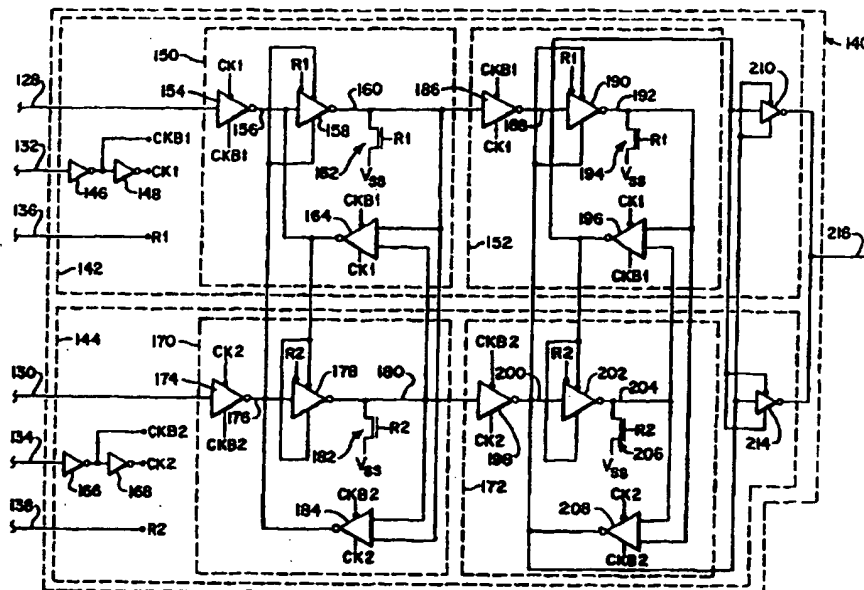
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(54) Title: HIGH RELIABILITY LOGIC CIRCUIT FOR A RADIATION ENVIRONMENT



(57) Abstract

According to the present invention, a dual memory cell, includes a first memory cell receiving a first logic state input signal and providing an output signal, and further includes a second memory cell receiving a second logic state input signal and providing an output signal, where the first and second memory cell and second memory cell further receive and respond to one or more logic signals from the other. The output signal of the first memory cells and the output signal of the second memory cell may be further combined to provide one resultant logic state output signal which has improved reliability in a radiation environment: